

INSTRUCTION PROCESSOR AND PROGRAMMABLE LOGIC DEVICE  
COOPERATIVE COMPUTING ARRANGEMENT AND METHOD

ABSTRACT

A method and arrangement for executing instructions of a computer program using a programmable logic device to perform selected functions of the program. Profile data for code segments of the computer program are generated during program execution. Based on the profile data, a code segment is selected for transformation to a hardware implementation. The functionality of the selected code segment is transformed into a configuration bitstream, and the PLD is configured with the configuration bitstream. During program execution, the PLD is activated in lieu of executing the code segment.